

SKM600GB07E3



Trench IGBT Modules

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Features*

- $V_{CE(sat)}$ with positive temperature coefficient
- High short circuit capability, self limiting to $6 \times I_{Cnom}$
- Fast & soft switching inverse CAL diodes
- Insulated copper baseplate using DCB Technology (Direct Copper Bonding)
- With integrated gate resistor

Typical Applications

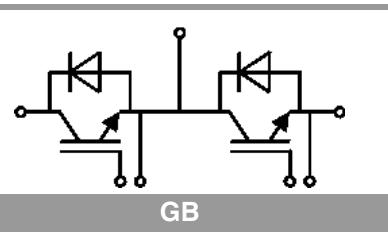
- AC inverter drives
- UPS
- Electronic welders

Remarks

- Case temperature limited to $T_c = 125^\circ\text{C}$ max.
- Recommended $T_{op} = -40 \dots +150^\circ\text{C}$
- Product reliability results valid for $T_j = 150^\circ\text{C}$
- Use of soft R_G necessary

Absolute Maximum Ratings		Values	Unit
Symbol	Conditions		
IGBT			
V_{CES}	$T_j = 25^\circ\text{C}$	650	V
I_C	$T_j = 175^\circ\text{C}$	852	A
		644	A
I_{Cnom}		600	A
I_{CRM}	$I_{CRM} = 3 \times I_{Cnom}$	1800	A
V_{GES}		-20 ... 20	V
t_{psc}	$V_{CC} = 360\text{ V}$ $V_{GE} \leq 15\text{ V}$ $V_{CES} \leq 650\text{ V}$	6	μs
T_j		-40 ... 175	$^\circ\text{C}$
Inverse diode			
V_{RRM}	$T_j = 25^\circ\text{C}$	650	V
I_F	$T_j = 175^\circ\text{C}$	812	A
		595	A
I_{Fnom}		600	A
I_{FRM}	$I_{FRM} = 2 \times I_{Fnom}$	1200	A
I_{FSM}	$t_p = 10\text{ ms, sin }180^\circ, T_j = 25^\circ\text{C}$	4320	A
T_j		-40 ... 175	$^\circ\text{C}$
Module			
$I_{t(RMS)}$		500	A
T_{stg}	module without TIM	-40 ... 125	$^\circ\text{C}$
V_{isol}	AC sinus 50 Hz, $t = 1\text{ min}$	4000	V

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 600\text{ A}$ $V_{GE} = 15\text{ V}$ chiplevel	1.45	1.90	1.90	V
		1.70	2.10	2.10	V
V_{CE0}	chiplevel	0.90	1.00	1.00	V
		0.82	0.90	0.90	V
r_{CE}	$V_{GE} = 15\text{ V}$ chiplevel	0.92	1.50	1.50	$\text{m}\Omega$
		1.47	2.00	2.00	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 9.6\text{ mA}$	5.1	5.8	6.4	V
I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}, T_j = 25^\circ\text{C}$			0.3	mA
C_{ies}	$V_{CE} = 25\text{ V}$ $f = 1\text{ MHz}$	37.0			nF
C_{oes}	$V_{GE} = 0\text{ V}$ $f = 1\text{ MHz}$	2.32			nF
C_{res}	$f = 1\text{ MHz}$	1.10			nF
Q_G	$V_{GE} = -8\text{ V} \dots +15\text{ V}$	4800			nC
R_{Gint}	$T_j = 25^\circ\text{C}$	0.5			Ω
$t_{d(on)}$	$V_{CC} = 300\text{ V}$	83			ns
t_r	$I_C = 600\text{ A}$ $V_{GE} = +15/-7.5\text{ V}$	121			ns
E_{on}	$R_{G\ on} = 3\ \Omega$	20			mJ
$t_{d(off)}$	$R_{G\ off} = 4.3\ \Omega$	1100			ns
t_f	$di/dt_{on} = 4900\text{ A}/\mu\text{s}$ $di/dt_{off} = 6700\text{ A}/\mu\text{s}$	93			ns
E_{off}	$dv/dt = 1330\text{ V}/\mu\text{s}$ $L_s = 20\text{ nH}$	37			mJ
$R_{th(j-c)}$	per IGBT		0.066		K/W
$R_{th(c-s)}$	per IGBT ($\lambda_{grease} = 0.81\text{ W}/(\text{m}^*\text{K})$)	0.033			K/W
$R_{th(c-s)}$	per IGBT, pre-applied phase change material	0.021			K/W





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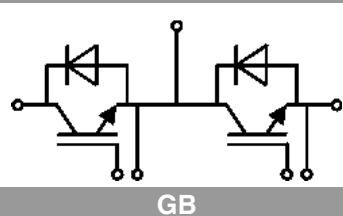
Typical Applications

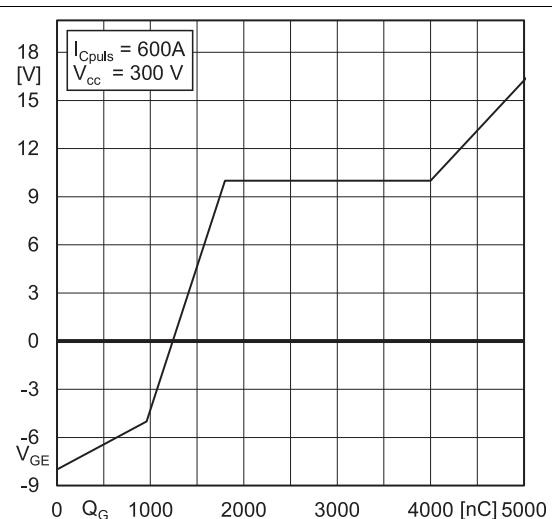
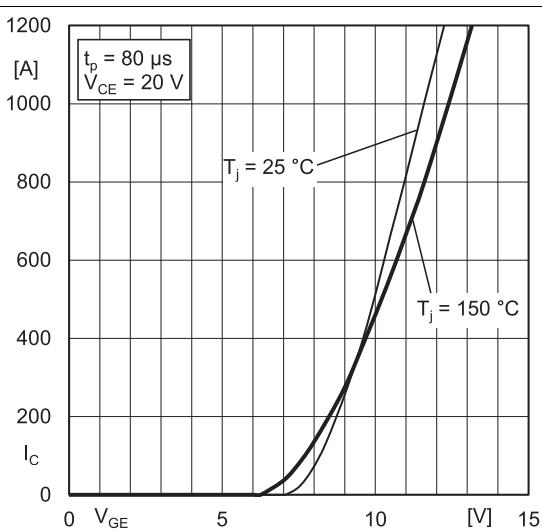
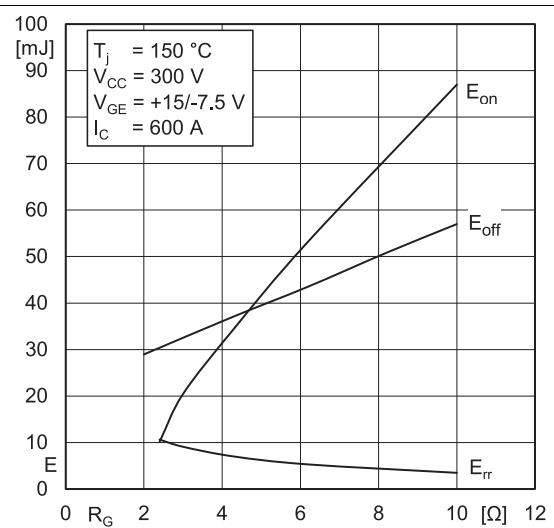
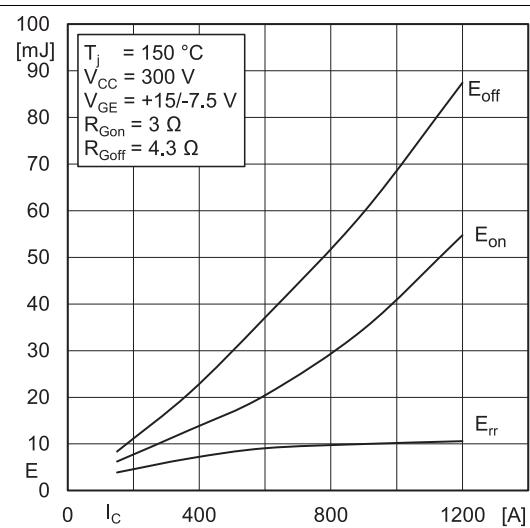
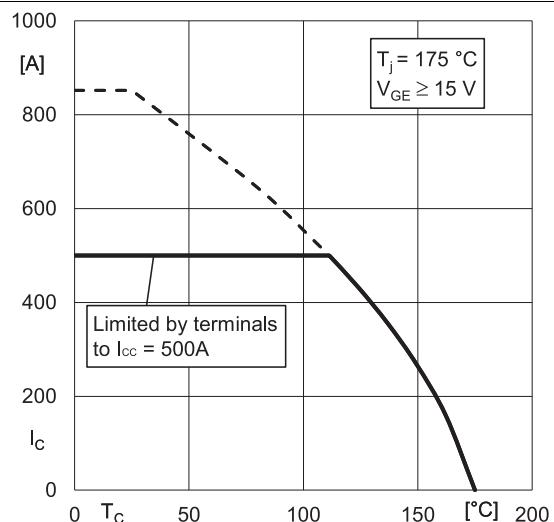
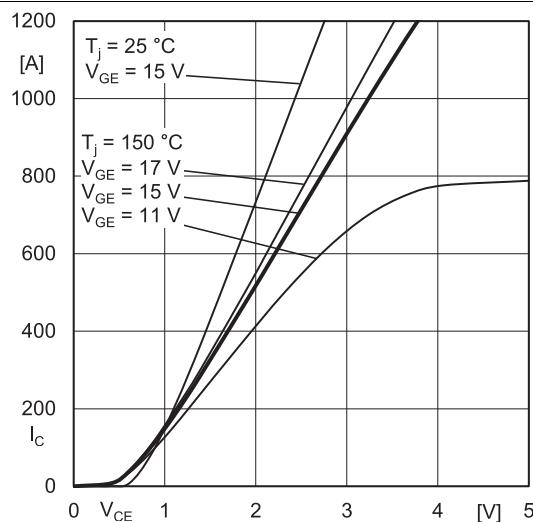
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Characteristics		Conditions	min.	typ.	max.	Unit
Symbol						
Inverse diode						
$V_F = V_{EC}$	$I_F = 600 \text{ A}$	$T_j = 25^\circ\text{C}$		1.40	1.76	V
	$V_{GE} = 0 \text{ V}$ chiplevel	$T_j = 150^\circ\text{C}$		1.38	1.77	V
V_{FO}	chiplevel	$T_j = 25^\circ\text{C}$		1.04	1.24	V
		$T_j = 150^\circ\text{C}$		0.85	0.99	V
r_F	chiplevel	$T_j = 25^\circ\text{C}$		0.60	0.88	$\text{m}\Omega$
		$T_j = 150^\circ\text{C}$		0.89	1.31	$\text{m}\Omega$
I_{RRM}	$I_F = 600 \text{ A}$	$T_j = 150^\circ\text{C}$		390		A
Q_{rr}	$dI/dt_{off} = 4940 \text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$		54		μC
E_{rr}	$V_{GE} = +15/-7.5 \text{ V}$	$V_{CC} = 300 \text{ V}$	$T_j = 150^\circ\text{C}$	9.1		mJ
$R_{th(j-c)}$	per diode			0.096		K/W
$R_{th(c-s)}$	per diode ($\lambda_{grease}=0.81 \text{ W}/(\text{m}^\circ\text{K})$)			0.038		K/W
$R_{th(c-s)}$	per diode, pre-applied phase change material			0.028		K/W
Module						
L_{CE}				15		nH
$R_{CC'EE'}$	measured per switch	$T_c = 25^\circ\text{C}$		0.55		$\text{m}\Omega$
		$T_c = 125^\circ\text{C}$		0.85		$\text{m}\Omega$
$R_{th(c-s)1}$	calculated without thermal coupling ($\lambda_{grease}=0.81 \text{ W}/(\text{m}^\circ\text{K})$)			0.0088		K/W
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module ($\lambda_{grease}=0.81 \text{ W}/(\text{m}^\circ\text{K})$)			0.014		K/W
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module, pre-applied phase change material			0.010		K/W
M_s	to heat sink M6			3	5	Nm
M_t		to terminals M6		2.5	5	Nm
w						Nm
				325		g





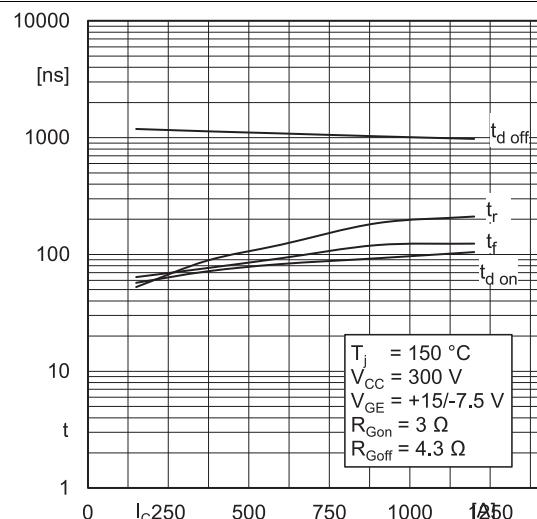


Fig. 7: Typ. switching times vs. I_C

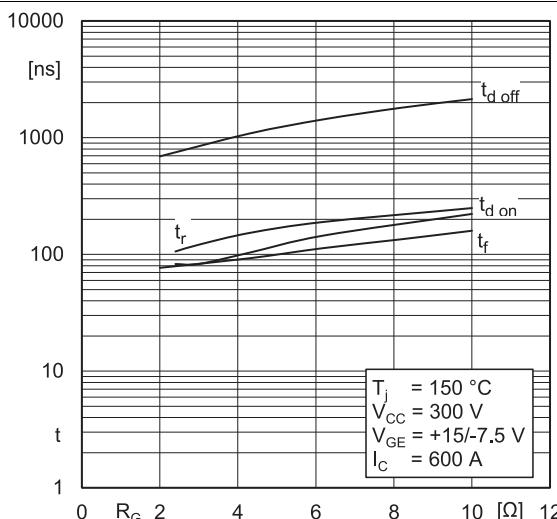


Fig. 8: Typ. switching times vs. gate resistor R_G

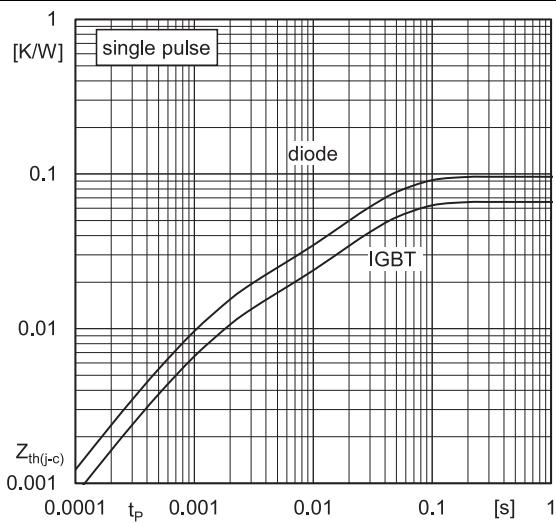


Fig. 9: Transient thermal impedance

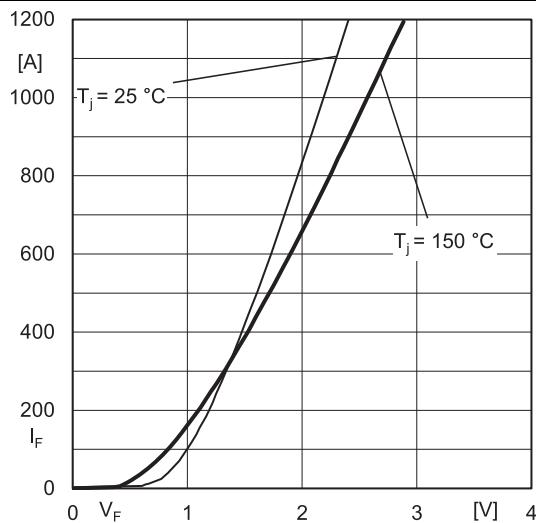


Fig. 10: Typ. CAL diode forward charact., incl. $R_{CC' + EE'}$

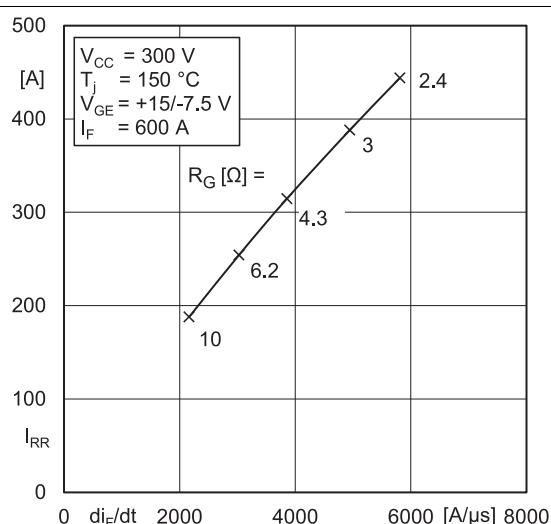


Fig. 11: Typ. CAL diode peak reverse recovery current

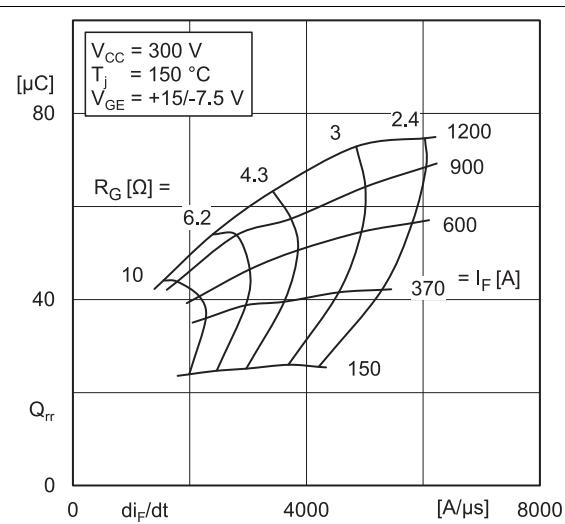
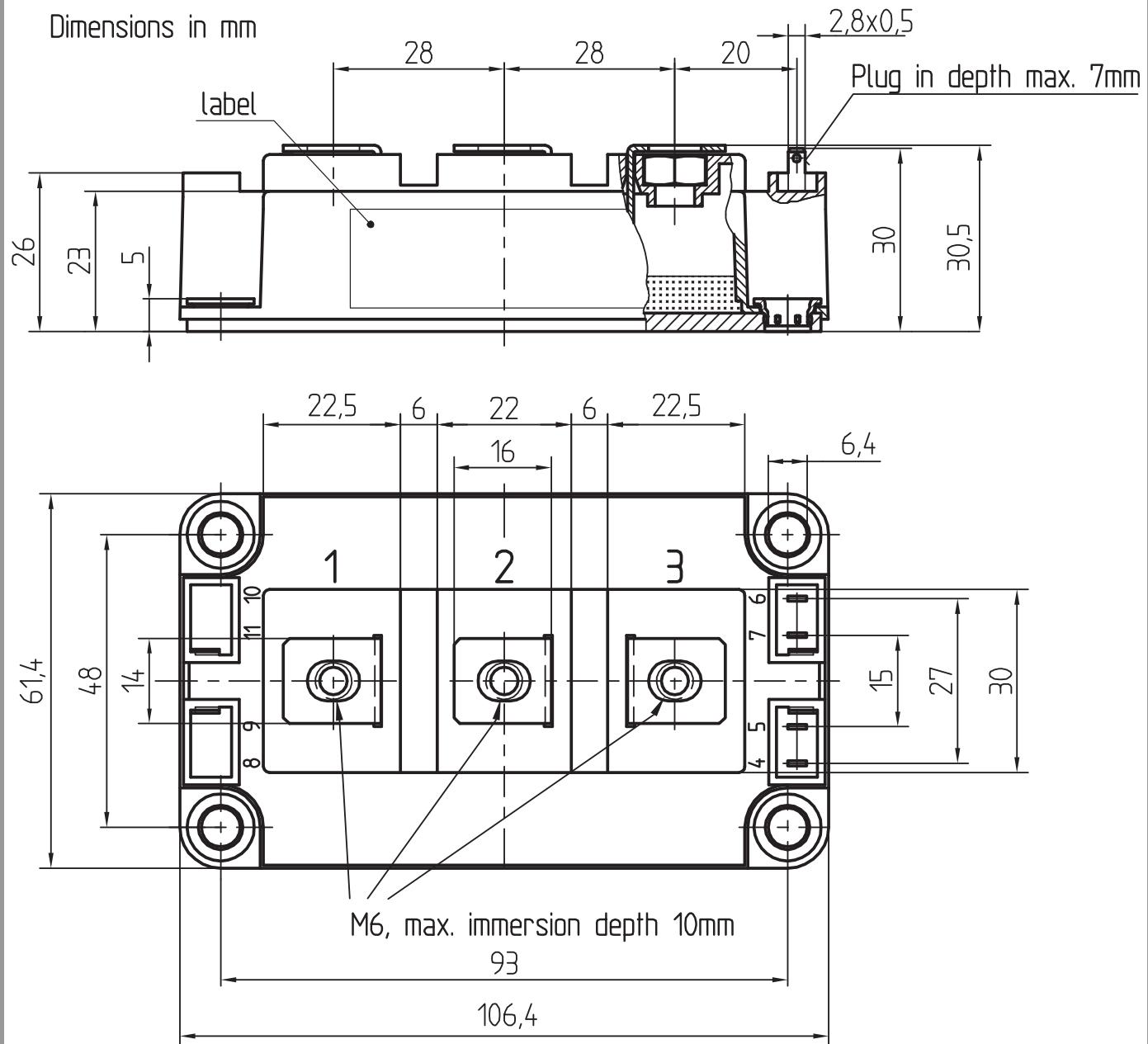


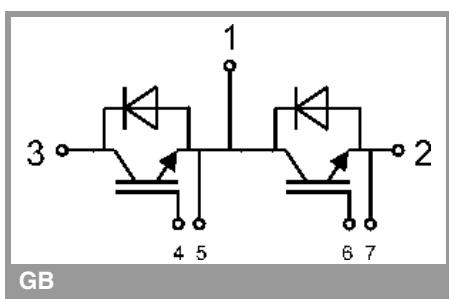
Fig. 12: Typ. CAL diode peak reverse recovery charge

Dimensions in mm



General tolerance $\pm 0,5$ mm

SEMITRANS 3



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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